

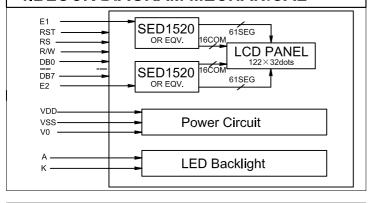
2.MECHANICAL SPECIFICATIONS

ITEM	SPECIFICATIONS	ITEM	REMARK
Module Size(L \times W \times H)	$65.8 \times 27.3 \times 8.4$	mm	
View Area(W×H)	54.8×18.3	mm	
Effective V/Area(W×H)	48.76×15.32	mm	Reference
Number of Dots	122×32	_	Dimensional Outline
Dot Pitch(W×H)	0.40×0.45	mm	Guime
Dot Size(W×H)	0.36×0.41	mm	
Weigh(Reflective/LED)	_	g	

3.ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITION	STANDARD		
I I E IVI		CONDITION	MIN	MAX	
Logic Voltage	Vdd		-0.3V	7V	
LCD Voltage	VLCD	Ta=25°C	-0.3V	_	
Input Voltage	VI		-0.3V	V _{DD} +0.3V	
Operation Temperature	Тор	_	-20°C	70℃	
Storage Temperature	Тѕт	_	-20℃	80℃	

4.BLOCK DIAGRAM MECHANICAL



5.LED BACKLIGHT SPECIFICATIONS

ITEM	SYMBOL	TYPE	MAX	UNIT		
Ta=25°C						
Forward Voltage	$ m V_{f}$	4.1	4.3	V		
Forward Current	If	140		mA		
Emission Vave Length	λ P	572	_	nm		

6.INTERFACE PIN CONNECTIONS

ITEM	SYMBOL	LEVEL	FUNCTIONS	
1	VDD	+5V	Power Supply For Logic	
2	VSS	0V	Power Ground	
3	V0	_	Contrast ajust	
4	RST	H/L	Reset Signal	
5	E1	H/L	Enable Signal For Chip A	
6	E2	H/L	Enable Signal For Chip B	
7	R/W	H/L	H:read L:write	
8	RS	H/L	H:data L:command	
9-16	DB0-DB7	H/L	Data Bus	
17	NC	_	No Connection	
18	NC	_	No Connection	

7.ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	MIN	TYPE	MAX	UNIT		
Ta=25°C							
Logic Power	V_{DD}	4.75	5	5.25	V		
Input High Voltage	Vih	2	_	Vdd	V		
Input Low Voltage	VIL	0	_	0.8	V		
Output High Voltage	Voh	2.4	_	Vdd	V		
Output Low Voltage	Vol	0	_	0.4	V		
Logic Current	Idd	1	_	1.5	mA		
Operation Voltage For LCD	Vdd-V0	_	5	_	V		